

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-42. (Canceled)

43. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming a conductive film electrically connected to a plurality of thin film transistors and a measurement wiring which are provided on an insulating surface;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors are defective from said value; and

forming a plurality of pixel electrodes by patterning said conductive film after said judging.

44. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming an interlayer insulating film to cover a plurality of thin film transistors provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a plurality of connecting wirings each of which are connected to source regions or drain regions of each of said plurality of thin film transistors through said contact holes, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said plurality of connecting wirings and said measurement wiring;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors are defective from said value; and

forming a plurality of pixel electrodes by patterning said conductive film after said judging.

45. (Original) A method of manufacturing a display panel according to claim 43, wherein EL layers and an opposing electrode are laminated in order on said plurality of pixel electrodes after said plurality of said pixel electrodes are formed.

46. (Original) A method of manufacturing a display panel according to claim 44, wherein EL layers and an opposing electrode are laminated in order on said plurality of pixel electrodes after said plurality of said pixel electrodes are formed.

47. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming an inspection conductive film electrically connected to a plurality of pixel electrodes each of which are electrically connected to each of a plurality of thin film transistors respectively, and a measurement wiring, which are provided on an insulating surface;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors and said plurality of pixel electrodes are defective from said value; and

removing said inspection conductive film.

48. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming an interlayer insulating film to cover a plurality of thin film transistors provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a plurality of connecting wirings each of which are connected to source regions or drain regions of each of said plurality of thin film transistors through said contact holes, respectively, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said plurality of connecting wirings;

forming a plurality of pixel electrodes by patterning said conductive film;

forming an inspection conductive film electrically connected to a plurality of pixel electrodes and said measurement wiring;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors and said plurality of pixel electrodes are defective from said value; and

removing said inspection conductive film.

49. (Previously Presented) A method of manufacturing a display panel according to claim 47, wherein EL layers and an opposing electrode are laminated in order on said plurality of pixel electrodes after said inspection conductive film is removed.

50. (Previously Presented) A method of manufacturing a display panel according to claim 48, wherein EL layers and an opposing electrode are laminated in order on said plurality of pixel electrodes after said inspection conductive film is removed.

51. (Original) A method of manufacturing a display panel according to claim 47, wherein said inspection conductive film is a fluid.

52. (Original) A method of manufacturing a display panel according to claim 48, wherein said inspection conductive film is a fluid.

53. (Previously Presented) A display panel formed by a method of manufacturing the display panel, the method comprising steps of:

forming a conductive film electrically connected to a plurality of thin film transistors and a measurement wiring which are provided on an insulating surface;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors are defective from said value; and

forming a plurality of pixel electrodes by patterning said conductive film after said judging.

54. (Previously Presented) A display panel formed by a method of manufacturing the display panel, the method comprising steps of:

forming an interlayer insulating film to cover a plurality of thin film transistors provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a plurality of connecting wirings each of which are connected to source regions or drain regions of each of said plurality of thin film transistors through said contact holes, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said plurality of connecting wirings and said measurement wiring;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors are defective from said value; and

forming a plurality of pixel electrodes by patterning said conductive film after said judging.

55. (Previously Presented) A display panel formed by a method of manufacturing the display panel, the method comprising steps of:

forming an inspection conductive film electrically connected to a plurality of pixel electrodes each of which are electrically connected to each of a plurality of thin film transistors respectively, and a measurement wiring, which are provided on an insulating surface;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors and said plurality of pixel electrodes are defective from said value; and

removing said inspection conductive film.

56. (Previously Presented) A display panel formed by a method of manufacturing the display panel, the method comprising steps of:

forming an interlayer insulating film to cover a plurality of thin film transistors provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a plurality of connecting wirings each of which are connected to source regions or drain regions of each of said plurality of thin film transistors through said contact holes, respectively, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said plurality of connecting wirings;

forming a plurality of pixel electrodes by patterning said conductive film;

forming an inspection conductive film electrically connected to a plurality of pixel electrodes and said measurement wiring;

making said plurality of thin film transistors in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said plurality of thin film transistors and said plurality of pixel electrodes are defective from said value; and

removing said inspection conductive film.

57. (Previously Presented) A method of manufacturing a display panel according to claim 43 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

58. (Previously Presented) A method of manufacturing a display panel according to claim 44 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

59. (Previously Presented) A method of manufacturing a display panel according to claim 47 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

60. (Previously Presented) A method of manufacturing a display panel according to claim 48 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

61. (Previously Presented) A method of manufacturing a display panel according to claim 43 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, and a controller to said display panel to be a display panel comprising an attached controller and driver circuit.

62. (Previously Presented) A method of manufacturing a display panel according to claim 44 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, and a controller to said display panel to be a display panel comprising an attached controller and driver circuit.

63. (Previously Presented) A method of manufacturing a display panel according to claim 47 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, and a controller to said display panel to be a display panel comprising an attached controller and driver circuit.

64. (Previously Presented) A method of manufacturing a display panel according to claim 48 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, and a controller to said display panel to be a display panel comprising an attached controller and driver circuit.

65. (Previously Presented) A method of manufacturing a display panel according to claim 43 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

66. (Previously Presented) A method of manufacturing a display panel according to claim 44 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

67. (Previously Presented) A method of manufacturing a display panel according to claim 47 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

68. (Previously Presented) A method of manufacturing a display panel according to claim 48 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

69. (Previously Presented) A method of manufacturing a display panel according to claim 43 further comprising a step of:

mounting said display panel to an electronic device wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera,

a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

70. (Previously Presented) A method of manufacturing a display panel according to claim 44 further comprising a step of:

mounting said display panel to an electronic device wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

71. (Currently Amended) A method of manufacturing a display panel according to claim 47 further comprising the steps of:

mounting said display panel to an electronic device wherein said the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

72. (Previously Presented) A method of manufacturing a display panel according to claim 48 further comprising the steps of:

mounting said display panel to an electronic device wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

73. (Previously Presented) A method of manufacturing of a display panel comprising a plurality of pixels each comprising a thin film transistor and a pixel electrode electrically connected to said thin film transistor, said method comprising steps of:

making said thin film transistor in an on state before forming said pixel electrode by patterning a conductive film;
measuring a value of electric current flowing in said conductive film; and
judging whether or not said thin film transistor is defective from said value,
wherein said steps are performed to said plurality of pixels in order.

74. (Previously Presented) A method manufacturing a display panel comprising a plurality of pixels each comprising a switching thin film transistor, an EL driver thin film transistor, a source signal line, a gate signal line, a power source supply line, and a pixel electrode electrically connected to said EL driver thin film transistor, said method comprising steps of:

making said switching thin film transistor in an on state by controlling an electric potential of said gate signal line, making said EL driver thin film transistor in an on state by controlling an electric potential of said source signal line, and controlling an electric potential of said power source supply line, before forming said pixel electrode by patterning a conductive film;

measuring a value of electric current flowing in said conductive film; and

judging whether or not said switching thin film transistor, said EL driver thin film transistor, said source signal line, said gate signal line, said power source supply line are defective from said value,

wherein said steps are performed to said plurality of pixels in order.

75. (Previously Presented) A method of manufacturing a display panel comprising a plurality of pixels each comprising a first switching thin film transistor, a second switching thin film transistor, an EL driver thin film transistor, a source signal line, a first gate signal line, a second gate signal line, a power source supply line, and a pixel electrode electrically connected to said EL driver thin film transistor, said method comprising steps of:

making said first switching thin film transistor in an on state by controlling an electric potential of said first gate signal line, making said EL driver thin film transistor in an on state by controlling an electric potential of said source signal line, and controlling an electric potential of said power source supply line, before forming said pixel electrode by patterning a conductive film;

measuring a first value of electric current flowing in said conductive film;

making said second switching thin film transistor in an on state by controlling an electric potential of said second gate signal line and making said EL driver thin film transistor in an off state, before forming said pixel electrode by patterning a conductive film;

measuring a second value of electric current flowing in said conductive film;

judging whether or not said first switching thin film transistor, said second switching thin film transistor, said EL driver thin film transistor, said source signal line, said first gate signal line, said second gate signal line, and said power source supply line are defective from said first value and said second value,

wherein said steps are performed to said plurality of pixels in order.

76. (Original) A method according to claim 73, wherein an EL layer and an opposing electrode are laminated in order on said pixel electrode after said pixel electrode is formed.

77. (Original) A method according to claim 74, wherein an EL layer and an opposing electrode are laminated in order on said pixel electrode after said pixel electrode is formed.

78. (Original) A method according to claim 75, wherein an EL layer and an opposing electrode are laminated in order on said pixel electrode after said pixel electrode is formed.

79. (Original) A method according to claim 73, wherein said value of electric current flowing in said conductive film is obtained by measuring a value of electric current flowing in a measurement wiring connected to said conductive film.

80. (Original) A method according to claim 74, wherein said value of electric current flowing in said conductive film is obtained by measuring a value of electric current flowing in a measurement wiring connected to said conductive film.

81. (Original) A method according to claim 75, wherein said first value and said second value of electric current flowing in said conductive film are obtained by measuring values of electric current flowing in a measurement wiring connected to said conductive film, respectively.

82. (Previously Presented) A method of manufacturing a display panel comprising a plurality of pixels each comprising a thin film transistor and a pixel electrode electrically connected to said thin film transistor, said method comprising steps of:

connecting the pixel electrodes of said plurality of pixels electrically by an inspection conductive layer;

making said thin film transistor in an on state;

measuring a value of electric current flowing in said inspection conductive film;

judging whether or not said thin film transistor is defective from said value; and

removing said inspection conductive film after said judging is performed to said plurality of pixels in order.

83. (Previously Presented) A method manufacturing a display panel comprising a plurality of pixels each comprising a switching thin film transistor, an EL driver thin film

transistor, a source signal line, a gate signal line, a power source supply line, and a pixel electrode electrically connected to said EL driver thin film transistor, said method comprising steps of:

- connecting the pixel electrodes of said plurality of pixels electrically by an inspection conductive layer;

- making said switching thin film transistor in an on state by controlling an electric potential of said gate signal line, making said EL driver thin film transistor in an on state by controlling an electric potential of said source signal line, and controlling an electric potential of said power source supply line;

- measuring a value of electric current flowing in said inspection conductive film;

- judging whether or not said switching thin film transistor, said EL driver thin film transistor, said source signal line, said gate signal line, said power source supply line are defective from said value; and

- removing said inspection conductive film after said judging is performed to said plurality of pixels in order.

84. (Previously Presented) A method of manufacturing a display panel comprising a plurality of pixels each comprising a first switching thin film transistor, a second switching thin film transistor, an EL driver thin film transistor, a source signal line, a first gate signal line, a second gate signal line, a power source supply line, and a pixel electrode electrically connected to said EL driver thin film transistor, said method comprising steps of:

- connecting the pixel electrodes of said plurality of pixels electrically by an inspection conductive layer;

- making said first switching thin film transistor in an on state by controlling an electric potential of said first gate signal line, making said EL driver thin film transistor in an on state by controlling an electric potential of said source signal line, and controlling an electric potential of said power source supply line;

measuring a first value of electric current flowing in said inspection conductive film;

making said second switching thin film transistor in an on state by controlling an electric potential of said second gate signal line and making said EL driver thin film transistor in an off state;

measuring a second value of electric current flowing in said inspection conductive film;

judging whether or not said first switching thin film transistor, said second switching thin film transistor, said EL driver thin film transistor, said source signal line, said first gate signal line, said second gate signal line, and said power source supply line are defective from said first value and said second value,

removing said inspection conductive film after said judging is performed to said plurality of pixels in order.

85. (Original) A method according to claim 82, wherein an EL layer and an opposing electrode are laminated in order on said pixel electrode after said inspection conductive film is removed.

86. (Original) A method according to claim 83, wherein an EL layer and an opposing electrode are laminated in order on said pixel electrode after said inspection conductive film is removed.

87. (Original) A method according to claim 84, wherein an EL layer and an opposing electrode are laminated in order on said pixel electrode after said inspection conductive film is removed.

88. (Original) A method according to claim 82, wherein said value of electric current flowing in said inspection conductive film is obtained by measuring a value of

electric current flowing in a measurement wiring connected to said inspection conductive film.

89. (Original) A method according to claim 83, wherein said value of electric current flowing in said inspection conductive film is obtained by measuring a value of electric current flowing in a measurement wiring connected to said inspection conductive film.

90. (Original) A method according to claim 84, wherein said first value and second value of electric current flowing in said inspection conductive film is obtained by measuring values of electric current flowing in a measurement wiring connected to said inspection conductive film, respectively.

91. (Original) A method according to claim 82, wherein said inspection conductive film is a fluid.

92. (Original) A method according to claim 83, wherein said inspection conductive film is a fluid.

93. (Original) A method according to claim 84, wherein said inspection conductive film is a fluid.

94. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming a conductive film electrically connected to a first thin film transistor, a second thin film transistor, and a measurement wiring which are provided on an insulating surface;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor are defective from said value; and

forming a first pixel electrode and a second pixel electrode by patterning said conductive film to be electrically connected to said first thin film transistor and said second thin film transistor, respectively.

95. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming an interlayer insulating film to cover a first thin film transistor and a second thin film transistor provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a first connecting wiring and a second connecting wiring respectively connected to each of source regions or drain regions of said first thin film transistor and said second thin film transistor through said contact holes, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said first connecting wiring, said second connecting wiring and said measurement wiring;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor are defective from said value; and

forming a first pixel electrode and a second pixel electrode by patterning said conductive film to be electrically connected to said first thin film transistor and said second thin film transistor, respectively.

96. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming an inspection conductive film electrically connected to a first pixel electrode and a second pixel electrode electrically connected to a first thin film transistor and a second thin film transistor, respectively, and a measurement wiring, which are provided on an insulating surface;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor and said first pixel electrode and said second pixel electrode are defective from said value; and

removing said inspection conductive film.

97. (Previously Presented) A method of manufacturing a display panel, comprising steps of:

forming an interlayer insulating film to cover a first thin film transistor and a second thin film transistor provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a first connecting wiring and a second connecting wiring respectively connected to each of source regions or drain regions of said first transistor and said second transistor through said contact holes, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said first connecting wiring and said second connecting wiring;

forming a first pixel electrode and a second pixel electrode electrically connected to said first thin film transistor and said second thin film transistor, respectively by patterning said conductive film;

forming an inspection conductive film electrically connected to said first pixel electrode and said second pixel electrode and said measurement wiring;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor and said first pixel electrode and said second pixel electrode are defective from said value; and

removing said inspection conductive film.

98. (Previously Presented) The active matrix type device display panel according to claim 94 wherein an EL layer and an opposing electrode are laminated in order on said first pixel electrode and said second pixel electrode after said first pixel electrode and said second pixel electrode is formed.

99. (Previously Presented) The display panel according to claim 95, wherein an EL layer and an opposing electrode are laminated in order on said first pixel electrode and said second pixel electrode after said first pixel electrode and said second pixel electrode is formed.

100. (Previously Presented) The display panel according to claim 96, wherein an EL layer and an opposing electrode are laminated in order on said first pixel electrode and said second pixel electrode after said inspection conductive film is removed.

101. (Previously Presented) The display panel according to claim 97, wherein an EL layer and an opposing electrode are laminated in order on said first pixel electrode and said second pixel electrode after inspection conductive film is removed.

102. (Previously Presented) The method of manufacturing a display panel according to claim 96, wherein said inspection conductive film is a fluid.

103. (Previously Presented) The method of manufacturing a display panel according to claim 97, wherein said inspection conductive film is a fluid.

104. (Previously Presented) A display panel formed by a method of manufacturing the display panel, the method comprising steps of:

forming a conductive film electrically connected to a first thin film transistor, a second thin film transistor, and a measurement wiring which are provided on an insulating surface;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor are defective from said value; and

forming a first pixel electrode and a second pixel electrode by patterning said conductive film to be electrically connected to said first thin film transistor and said second thin film transistor, respectively.

~~105. (Previously Presented) A display panel formed by a method of~~
manufacturing the display panel, the method comprising steps of:

forming an interlayer insulating film to cover a first thin film transistor and a second thin film transistor provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a first connecting wiring and a second connecting wiring respectively connected to each of source regions or drain regions of said first thin film transistor and said second thin film transistor through said contact holes, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said first connecting wiring, said second connecting wiring and said measurement wiring;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor are defective from said value; and

forming a first pixel electrode and a second pixel electrode by patterning said conductive film to be electrically connected to said first thin film transistor and said second thin film transistor, respectively.

106. (Previously Presented) A display panel formed by a method of manufacturing the display panel, the method comprising steps of:

forming an inspection conductive film electrically connected to a first pixel electrode and a second pixel electrode electrically connected to a first thin film transistor and a second thin film transistor, respectively, and a measurement wiring, which are provided on an insulating surface;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor and said first pixel electrode and said second pixel electrode are defective from said value; and

removing said inspection conductive film.

107. (Previously Presented) A display panel formed by a method of manufacturing a display panel, comprising steps of:

forming an interlayer insulating film to cover a first thin film transistor and a second thin film transistor provided on an insulating surface;

forming contact holes in said interlayer insulating film;

forming a first connecting wiring and a second connecting wiring respectively connected to each of source regions or drain regions of said first transistor and said second transistor through said contact holes, and a measurement wiring in contact with said insulating surface;

forming a conductive film electrically connected to said first connecting wiring and said second connecting wiring;

forming a first pixel electrode and a second pixel electrode electrically connected to said first thin film transistor and said second thin film transistor, respectively by patterning said conductive film;

forming an inspection conductive film electrically connected to said first pixel electrode and said second pixel electrode and said measurement wiring;

making said first thin film transistor and said second thin film transistor in an on state, measuring a value of electric current flowing in said measurement wiring, and judging whether or not said first thin film transistor and said second thin film transistor and said first pixel electrode and said second pixel electrode are defective from said value; and

removing said inspection conductive film.

108. (Previously Presented) A method of manufacturing a display panel according to claim 94 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

109. (Previously Presented) A method of manufacturing a display panel according to claim 95 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

110. (Previously Presented) A method of manufacturing a display panel according to claim 96 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

111. (Previously Presented) A method of manufacturing a display panel according to claim 97 further comprising a step of:

attaching a source signal line driver circuit and a gate signal line driver circuit to said display panel to be a display panel comprising an attached driver circuit.

112. (Previously Presented) A method of manufacturing a display panel according to claim 94 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

113. (Previously Presented) A method of manufacturing a display panel according to claim 95 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

114. (Previously Presented) A method of manufacturing a display panel according to claim 96 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

115. (Previously Presented) A method of manufacturing a display panel according to claim 97 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

116. (Previously Presented) A method of manufacturing a display panel according to claim 94 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

117. (Previously Presented) A method of manufacturing a display panel according to claim 95 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

118. (Previously Presented) A method of manufacturing a display panel according to claim 96 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

119. (Previously Presented) A method of manufacturing a display panel according to claim 97 further comprising a step of:

attaching a source signal line driver circuit, a gate signal line driver circuit, a controller, and a microcomputer to said display panel to be a display panel comprising an attached microcomputer, controller and driver circuit.

120. (Previously Presented) A method of manufacturing a display panel according to claim 94 further comprising a step of:

mounting said display panel to an electronic device,

wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

121. (Previously Presented) A method of manufacturing a display panel according to claim 95 further comprising a step of:

mounting said display panel to an electronic device,

wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

122. (Previously Presented) A method of manufacturing a display panel according to claim 96 further comprising a step of:

mounting said display panel to an electronic device,

wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.

123. (Previously Presented) A method of manufacturing a display panel according to claim 97 further comprising a step of:

mounting said display panel to an electronic device,

wherein the electronic device is selected from the group consisting of an EL display, a video camera, a digital camera, a goggle display device, a navigation system, a sound-reproducing system, a notebook type personal computer, a portable information terminal, and a DVD device.